

**UNIVERSITY COLLEGE OF ENGINEERING
OSMANIA UNIVERSITY, HYDERABAD - 7**

**M. E. (ECE, Mechanical & CSE) II - Semesters (Main) Examination
July 2018**

**M V S R ENGINEERING COLLEGE
REVISED EXAMINATION TIME-TABLE**

Time : 2.00 PM to 5.00 PM

DATE & DAY	E. C. E. (EMBEDDED SYSTEMS & VLSI DESIGN)	MECHANICAL (CAD / CAM)	C. S. E.
	II-Semester	II-Semester	II-Semester
02-07-2018 MONDAY	VLSI Physical Design	Flexible Manufacturing Systems	Data Mining
04-07-2018 WEDNESDAY	Field Programmable Gate Arrays	Automation	Advanced Databases
06-07-2018 FRIDAY	Low Power VLSI Design	Additive Manufacturing Technologies & Applications	=====
09-07-2018 MONDAY	Real Time Operating Systems	Vibration Analysis & Condition Monitoring	Distributed Computing
11-07-2018 WEDNESDAY	Digital IC Design	=====	Soft Computing
13-07-2018 FRIDAY	=====	Computer Aided Modelling & Design	Software Project Management
18-07-2018 WEDNESDAY	Digital System Design	=====	Network Security
20-07-2018 FRIDAY	=====	Computer Aided Mechanical Design & Analysis	=====


**DIRECTOR OF EVALUATION
EXAMINATION CELL**